

**I CLAIM:**

1. A multi-layer circuit board comprising:

first, second, third, fourth, fifth, sixth and  
seventh insulating substrates disposed sequentially  
5 one above the other;

a first signal wiring layer disposed on one side of  
said first insulating substrate opposite to said second  
insulating substrate;

a first ground wiring layer disposed between said  
10 first and second insulating substrates;

a second signal wiring layer disposed between said  
second and third insulating substrates;

a third signal wiring layer disposed between said  
third and fourth insulating substrates;

15 a power wiring layer disposed between said fourth  
and fifth insulating substrates;

a fourth signal wiring layer disposed between said  
fifth and sixth insulating substrates;

a second ground wiring layer disposed between said  
20 sixth and seventh insulating substrates; and

a fifth signal wiring layer disposed on one side of  
said seventh insulating substrate opposite to said  
sixth insulating substrate;

wherein each of said first and seventh insulating  
25 substrates has a thickness ranging from 2.5 to 6.5 mil;

wherein each of said second, fourth and sixth  
insulating substrates has a thickness ranging from 3

to 9 mil;

wherein each of said third and fifth insulating substrates has a thickness ranging from 3 to 23 mil;

wherein said first signal wiring layer has a first  
5 resistance with respect to said first ground wiring layer, said second signal wiring layer having a second resistance with respect to said first ground wiring layer and said power wiring layer, said third signal wiring layer having a third resistance with respect to  
10 said first ground wiring layer and said power wiring layer, said fourth signal wiring layer having a fourth resistance with respect to said second ground wiring layer and said power wiring layer, said fifth signal wiring layer having a fifth resistance with respect to  
15 said second ground wiring layer; and

wherein said first, second, third, fourth and fifth resistances are within the range of 49.5 to 60.5 ohms.

2. The multi-layer circuit board of Claim 1, wherein at least one of said first, third, fifth and seventh  
20 insulating substrates is made from a polyester prepreg.

3. The multi-layer circuit board of Claim 1, wherein at least one of said second, fourth and sixth insulating substrates is made from a fibrous core material.

4. The multi-layer circuit board of Claim 3', wherein  
25 the core material contains paper fibers.

5. The multi-layer circuit board of Claim 3, wherein the core material contains glass fibers.

6. The multi-layer circuit board of Claim 1, wherein the thicknesses of said first and seventh insulating substrates are equal.

5 7. The multi-layer circuit board of Claim 1, wherein the thicknesses of said second and sixth insulating substrates are equal.

8. The multi-layer circuit board of Claim 1, wherein the thicknesses of said third and fifth insulating substrates are equal.

10 9. The multi-layer circuit board of Claim 1, wherein: each of said first and fifth signal wiring layers has a thickness of about 1.4 mil; and

15 each of said second, third and fourth signal wiring layers, said first and second ground wiring layers, and said power wiring layer has a thickness of about 0.7 mil.

10. The multi-layer circuit board of Claim 1, wherein each of said third and fifth insulating substrates has a thickness ranging from 3 to 9 mil.

20 11. The multi-layer circuit board of Claim 10, wherein: each of said first and seventh insulating substrates has a thickness of 4.5 mil;

each of said second, third, fourth, fifth and sixth insulating substrates has a thickness of 6 mil; and

25 said first, second, third, fourth, fifth, sixth and seventh insulating substrates, said first, second, third, fourth and fifth signal wiring layers, said

first and second ground wiring layers, and said power wiring layer are press-bonded to each other to form said circuit board with a thickness of about 1.2 mm.

12. The multi-layer circuit board of Claim 1, wherein each of said third and fifth insulating substrates has a thickness ranging from 9 to 23 mil.

13. The multi-layer circuit board of Claim 12, wherein: each of said first and seventh insulating substrates has a thickness of 4.5 mil;

10 each of said second, fourth and sixth insulating substrates has a thickness of 6 mil;

each of said third and fifth insulating substrates has a thickness of 16 mil; and

15 said first, second, third, fourth, fifth, sixth and seventh insulating substrates, said first, second, third, fourth and fifth signal wiring layers, said first and second ground wiring layers, and said power wiring layer are press-bonded to each other to form said circuit board with a thickness of about 1.6 mm.